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Hon. Assistant Commissioner for Patents
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Washington, D.C. 20231

RE: New Patent Application in U.S.

Applicant(s): Eric JACQUINOT et al.

Title: NEW ABRASIVE COMPOSITION FOR THE
INTEGRATED CIRCUITS ELECTRONICS INDUSTRY

Atty's Docket: JACQUINOT-7

Sir:

Attached herewith is the above-identified application for Letters Patent including:

- Specification (16 pages), claims (2 pages) and abstract (1 page)
- _____ Sheets Drawings (Figures 1-)
- Formal Informal
- The inventors of this application are:
Eric JACQUINOT of 4, rue de la Pierre Sautee,
60350 Trosly Breuil, France;
Pascal LETOURNEAU of 1304, route de Tuteigny, 01170 Cessy, France; and
Maurice RIVOIRE of 38, Chemin de la Taillat, 38240 Meylan, France.
The inventors are citizens of France.
- Preliminary Amendment
- Supplemental Preliminary Amendment
- Information Disclosure Statement with 1449 and 7 references
- Return Receipt Postcard (in duplicate)

The following statements are applicable:

- The benefit under 35 USC §119 is claimed of the filing date of: France Application No. 9814073 in France on November 9, 1998. A certified copy of said priority document is attached.
- The present application is a Continuation Divisional Continuation-in-part of prior application No. _____. Although this application is stated to be a CIP, applicant does not concede that any matter is presented in this application which is not present in the parent.
- Incorporation By Reference. The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied herewith, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.

[] The undersigned attorney of record hereby appoints associate power of attorney, to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith to:

[X] In accordance with 37 CFR 1.53(a) and (b), it is respectfully requested that a serial number and filing date be assigned to this application as of the date of receipt of the present papers. In accordance with the present procedures of the U.S. Patent and Trademark Office, an executed Declaration and the filing fee for the present application will be filed in due course.

[X] **No authorization is given for charging the filing fee at the present time. However, at such time that the declaration is filed, but not before, you are authorized to charge whatever excess fees are necessary (including the filing fee and any extension of time fees then due) to Deposit Account 02-4035, if any such fees due are not fully covered by check filed at that time.**

[X] The attorneys of record in this application will be Sheridan Neimark, Reg. No. 20,520; Roger L. Browdy, Reg. No. 25,618; Anne M. Kornbau, Reg. No. 25,884; Norman J. Latker, Reg. 19,963; Iver P. Cooper, Reg. No. 28,005; *Allen C. Yun, Reg. No. 37,971 and Nick Bromer, Reg. No. 33,478 (*Patent Agent). Please send all correspondence with respect to this case to:

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419 Seventh Street, N.W.
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Please direct all telephone calls to Browdy and Neimark at (202) 628-5197.

[X] The Commissioner is hereby authorized to credit any overpayment of fees accompanying this paper to Deposit Account No. 02-4035.

Respectfully submitted,
BROWDY AND NEIMARK, P.L.L.C.

By: N. Latker
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)	Art Unit:
)	
Eric JACQUINOT et al.)	Washington, D.C.
)	
U.S.S.N.:)	October 27, 1999
)	
Filing Date: October 27, 1999)	
)	
For: NEW ABRASIVE...)	Docket No.: JACQUINOT=7

PRELIMINARY AMENDMENT

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Sir:

Contemporaneous with the filing of this case and prior
to calculation of the filing fee, kindly amend as follows:

IN THE CLAIMS

Claim 3, line 1, delete "one of claims 1 to 2", and
insert therefor --claim 1--.

Claim 4, line 1, delete "one of claims 1 to 3", and
insert therefor --claim 1--.

Claim 5, line 1, delete "one of claims 1 to 4", and
insert therefor --claim 1--.

Claim 6, line 1, delete "one of claims 1 to 5", and
insert therefor --claim 1--.

Claim 7, line 1, delete "one of claims 1 to 6", and
insert therefor --claim 1--.

Claim 8, line 1, delete "one of claims 1 to 7", and
insert therefor --claim 1--.

Claim 9, line 1, delete "one of claims 1 to 8", and
insert therefor --claim 1--.

Claim 10, line 1, delete "one of claims 1 to 9", and insert therefor --claim 1--.

Claim 11, line 1, delete "one of claims 1 to 10", and insert therefor --claim 1--.

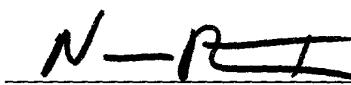
REMARKS

The above amendments to the claims are being made in order to eliminate multiple dependency and for the purpose of reducing the filing fee. Please enter this amendment prior to calculation of the filing fee in this case.

Favorable consideration and allowance are earnestly solicited.

Respectfully submitted,

BROWDY AND NEIMARK, P.L.L.C.
Attorneys for Applicants

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BIF022111/US

PATENT OF INVENTION

Applicants

Eric JACQUINOT

Pascal LETOURNEAU

Maurice RIVOIRE

TITLE

New abrasive composition for the integrated circuits electronics industry.

PRIORITY CLAIM

PRIORITY

French Patent Application no. 98 14073 filed on 9th November 1998

5

The present invention relates to new abrasive compositions which can be used in particular during mechanical chemical polishing of silicon oxide layers, silicon nitride layers and layers based on polymers with a low dielectric constant used in the semi-conductors microelectronics industry and, more 10 particularly, during the production of integrated circuits.

Basic colloidal-silica based abrasive compositions and surfactants are used for polishing primary silicon. This primary operation is followed by a certain number of other operations with the aim of producing integrated circuits.

During the production of integrated circuits, it can be useful to have 15 abrasive mechanical chemical polishing compositions which have a high selectivity for the polishing speed between silicon oxide and silicon nitride, the selectivity being the ratio between the abrasion speeds of each substrate. Silicon oxide can either be obtained by oxygen diffusion at a high temperature on silicon wafer (thermal oxide or SiO₂) or deposited on the silicon wafer (for example 20 tetraethoxysilane oxide or TEOS).

When producing integrated circuits, one of the first stages consists of defining the active zones of different transistors which constitute the circuits. These active zones should be isolated from each other by means of a silicon oxide zone, an electrically isolating material. These silicon oxide patterns were up to now most 25 often defined by the local silicon oxidation technique (LOCOS).

The reduction of the dimensions of devices now precludes this technique, the main disadvantage of which is the production of an undesired area bordering the oxidation zone, called a bird's mouth. LOCOS oxidation today therefore is mostly replaced by the STI technique (shallow trench isolation) which 30 consists of generating steep trenches in the silicon by reactive ionic engraving,

then filling them with a TEOS type oxide deposit.

This silicon oxide layer should then be levelled by a mechanical chemical polishing stage. An enhanced degree of levelling for this stage is essential.

5 One way of improving the levelling quality is to deposit a layer of silicon nitride on the ridges between the trenches (in practice, the silicon nitride is usually deposited over the whole surface of the silicon wafer, then the whole is recess-engraved to form the trenches, and because of this, the silicon nitride is left on the ridges between the trenches), then to deposit a layer of silicon oxide to fill 10 up the trenches. This excess oxide overflows the trenches and covers the silicon nitride.

The silicon oxide layer is then levelled using abrasive compositions until the silicon nitride layer is reached, which thus plays the role of a layer 15 resistant to the abrasion on condition that the selectivity of the polishing speeds between silicon oxide and silicon nitride is high.

In the literature, G.G. Lisle et al, US 5 759 917, described an aqueous mechanical chemical polishing suspension comprising 0.1 to 4% by weight of ceric ammonium nitrate and 0.1 to 3% by weight of acetic acid. This polishing suspension with a pH comprised between 3.8 and 5.5 has a silicon 20 PETEOS/nitrate polishing selectivity less than or equal to 35, i.e. the silicon oxide is abraded 35 times faster than the silicon nitride. One part of the ceric ammonium nitrate can be replaced by the pyrogenic silica.

Moreover, S.D. Hosali et al, US 5 738 800, mentioned the polishing of a composite comprising silica and silicon nitride by a polishing composition 25 comprising 0.2 to 5% by weight of cerium oxide, 0.5 to 3.5% potassium hydrogen phtalate as chelating agent, and 0.1 to 0.5% of fluorinated surfactant. The composition was brought to a neutral pH by the addition of a base. Silica can be added to the polishing compositions. With the best compositions described, a polishing selectivity of approximately 300 can be achieved.

30 Similarly, during the production of integrated circuits, the electronic

devices formed on a silicon wafer should be connected to each other by means of metal interconnection lines to compose the desired electronic circuit. Each metal interconnection level should be isolated electrically. To this end, each level is encapsulated in a dielectric layer.

5 The metallic interconnection lines of integrated circuits are mostly formed by the reactive ionic engraving of metal according to the following sequence: an aluminium or aluminium alloy film approximately 10-12m thick is deposited by a beam of electrons or ions (sputtering), the design of the interconnection circuit is then transferred onto it by photolithography then by

10 Reactive Ionic Etching (RIE). The lines thus defined are then recovered by a dielectric layer, usually silicon oxide based, mostly obtained by decomposition in vaporous phase of tetraethylorthosilicate (TEOS). This layer is then levelled by mechanical chemical polishing.

In order to reduce the capacities induced by this dielectric, one
 15 method consists of using materials with low dielectric constants. The best candidates were listed by N.H. Hendricks – Mat. Res. Soc. Symp. Proc., vol. 443, 1997 p. 3-14, and by L. Peters – Semi Conductor International – Sept. 1998, p. 64-74.

The main families retained are:

20 - fluorinated poly(aryl ethers) such as Flare®,
 - poly(aryl ethers) such as PAE-2,
 - fluorinated polyamides,
 - hydridosilsesquioxanes and alkylsilsesquioxanes,
 - bisbenzocyclobutenes such as Cyclotene®,

25 - poly(p-xylylene) such as Parylene®N and poly(α , α , α' , α' -tetrafluoro-p-xylylene) such as Parylene®F,
 - aromatic ether polymers of perfluorocyclobutane,
 - aromatic hydrocarbons such as SiLK®.

To be able to be integrated in a microelectronic interconnection
 30 process, these polymers with a low dielectric constant must satisfy in principle the

following criteria:

- filling trenches of width less than 0.35 pm
- a vitreous transition temperature higher than 400°C,
- a low humidity absorption (<1%),

5 - an enhanced thermal stability (<1%/h at 450°C),

- an enhanced adhesion to the metal layers and other dielectric layers.

Moreover, the polymer layer deposited must be able to be subsequently levelled by mechanical chemical polishing.

Y.L. Wang et al in *Thin Solid Films*, (1997), 308-309, p. 550-554, 10 polished an alkyl siloxane layer by means of an abrasive solution comprising fumed silica particles in base medium (pH = 10.2) or zirconium oxide particles in acid medium (pH = 3.3-4.6).

G. R. Yang et al in *J. of Electronic Materials*, Vol. 26, no. 8, 1997, p. 935-940, studied the surface quality of a layer of Parylene®N obtained after 15 polishing with different abrasive solutions comprising aluminium particles in acid or alkaline medium. The polishing speeds obtained are low whatever the abrasive solution used.

J. M. Neirynck et al in *Mater. Res. Soc. Symp. Proc.*, 1995, vol. 381, p. 229-235) attempted to polish three types of polymers with a low dielectric 20 constant, Parylene®, benzocyclobutene (BCB) and a fluorinated polyimide, by means of abrasive solutions comprising aluminium particles in a base or acid medium.

Currently, the integration of polymer layers with a low dielectric constant in a microelectronic interconnection process is running into difficulties 25 principally with insufficient performances obtained on this type of material during the levelling stage by mechanical chemical polishing. This stage is not yet fully mastered, in particular in relation to the polishing speed and the surface state of the polished surface.

It was determined in surprising and unexpected manner that the use 30 of an aqueous acid suspension of cationised colloidal silica containing colloidal

silica particles not linked to each other by siloxane bonds, allows a layer of a polymer-based isolating material with a low dielectric constant, in particular of SiLK® type, to be polished resulting in a very good compromise on the polishing speed, polishing uniformity and surface state properties of the polished surface.

5 A subject of the present Application is therefore a mechanical chemical polishing composition for a layer made of polymer-based isolating material with a low dielectric constant, characterized in that said polishing composition comprises an aqueous acid suspension of cationised colloidal silica containing individualised colloidal silica particles not linked to each other by
10 siloxane bonds and water as suspension medium.

By "aqueous acid suspension of cationised colloidal silica" is meant an aqueous acid suspension of colloidal silica surface-modified by tri- or tetravalent metal oxides such as aluminium, chromium, gallium, titanium or zirconium and which are in particular described in the book "The Chemistry of Silica – R. K. Iler-
15 Wiley Interscience (1979) p. 410-411".

Under preferential conditions of using the invention, the aqueous acid solution of cationised colloidal silica containing colloidal silica particles not linked to each other by siloxane bonds is obtained using a solution of aluminium hydroxychloride at approximately 50% by weight in which an alkaline silica sol
20 stabilised with sodium is introduced under agitation at a pH of approximately 9. A stable suspension of cationic colloidal silica particles of pH comprised between 3.5 and 4 is thus obtained.

In the present Application, by "polymer-based isolating material with a low dielectric constant of SiLK® type" is meant a material as described by P.H.
25 Townsend et al in Mat. Res. Soc. Symp. Proc. 1997, 476, p. 9-17. This material is constituted by an oligomeric solution of viscosity of 30 mPa.s, the polymerisation of which does not require a catalyst and does not induce the formation of water. The polymerised network is an aromatic hydrocarbon which does not contain fluorine. Its dielectric constant is 2.65, its vitreous transition temperature is higher than
30 490°C and its refraction index is 1.63.

It would therefore be desirable to have a composition providing a high abrasion selectivity.

Thus, the Applicant determined in surprising and unexpected manner that the addition of a low quantity of surfactant to an aqueous acid suspension of individualised colloidal silica particles not linked to each other by siloxane bonds allowed the polishing speed of the silicon nitride (Si_3N_4) to be reduced very considerably whilst preserving the polishing speed of the silicon oxide (thermal oxide or tetraethoxysilane oxide). This translates as a considerable increase in the silicon oxide/silicon nitride polishing selectivity which can reach values of up to 10 500.

The Applicant also determined in surprising and unexpected manner that the addition of a low quantity of surfactant to an aqueous acid suspension of individualised colloidal silica particles not linked to each other by siloxane bonds allowed the polishing speed of polymers with a low dielectric constant to be 15 increased very considerably.

A subject of the present invention is thus an abrasive composition for the integrated circuits industry, characterized in that it comprises or is preferably constituted by an aqueous acid suspension of individualised colloidal silica particles not linked to each other by siloxane bonds and a surfactant.

20 The colloidal silica particles of aqueous acid suspensions of the present invention advantageously have an average diameter comprised between 12 and 100 nm, preferably between 35 and 50 nm.

Such aqueous suspensions of colloidal silica can be obtained either from alkaline silicates, in particular sodium or potassium, or from ethyl silicate. In 25 these two cases, using means known to the person skilled in the art and described in more detail in KK. Iler in "The Chemistry of silica", chapter 9, pages 331 to 343, published by Wiley Interscience, 1979, aqueous acid suspensions of colloidal silica containing individualised particles not linked to each other by siloxane-type bonds of diameter comprised between 12 and 100 nm can be obtained.

30 The pH of such aqueous acid suspensions of colloidal silica is

preferably comprised between 1 and 5 and most particularly between 2 and 3.

A particular subject of the present Application is an abrasive composition, as above, in which the concentration of the polishing substance, i.e., the colloidal silica, is comprised between 5 and 50%, notably between 20 and 40%, 5 particularly between 25 and 35% and most particularly approximately 30% by weight.

The surfactant used allows the polishing speed of the silicon nitride to be reduced very considerably whilst preserving the polishing speed of the silicon oxide. A selective polishing of the silicon oxide relative to the silicon nitride is thus 10 obtained. It also allows the polishing speed of polymers with a low dielectric constant to be increased very considerably.

The quantity of surfactant(s) used in the polishing composition advantageously corresponds to a volumic concentration comprised between 0.001% and 5%, notably comprised between 0.005 and 2% and very preferably to 15 a volumic concentration of 0.01 to 1%. The surfactants used can be commercially available products such as for example those cited below in the examples.

Preferably, the polishing composition according to the invention contains less than 5% of an oxidising agent and more particularly no oxidising agent at all.

20 It was determined that if polishing compositions having a good long-term preservation time were to be formed, anionic or non-ionic type surfactants would preferably have to be used, as opposed to cationic surfactants which tend to destabilise colloidal silica based aqueous acid polishing solutions and thus to diminish the preservation time of the abrasive compositions. Very preferably, 25 anionic surfactants for which the Applicant obtained unexpected and very convincing results will be used.

The compositions according to the invention have remarkable qualities and in particular:

30 - an $\text{SiO}_2/\text{Si}_3\text{N}_4$ polishing selectivity being able to be adjusted over an extensive range of values (5 to 500) according to the concentration of surfactants,

increasing the concentration usually increases the selectivity,

- an excellent surface state of polished silicon oxide layer,
- an improved silicon oxide polishing uniformity,
- a very good levelling effect,

5 - a very good polishing speed for polymers with a low dielectric constant.

They justify the use of compositions according to the invention, in particular for the formation of abrasives for the integrated circuits industry.

For this reason, a subject of the present invention is also an abrasive for mechanical chemical polishing in the integrated circuits industry, characterized 10 in that it comprises a fabric impregnated with an aqueous acid suspension of colloidal silica of pH comprised between 1 and 5 containing individualised particles not linked to each other by siloxane bonds of diameter comprised between 12 and 100 nm, and a surfactant preferably of anionic or non-ionic type.

Finally, a subject of the invention is a process for mechanical 15 chemical polishing in the integrated circuits industry in which a silicon oxide layer, a silicon nitride layer and a layer of polymers with a low dielectric constant is rubbed with a support impregnated with an abrasive composition, characterized in that the abrasive composition comprises an aqueous acid suspension of individualised colloidal silica particles not linked to each other by siloxane bonds and a surfactant.

20 The preferred conditions indicated above for the abrasive compositions of the invention apply to other subjects of the invention.

The scope of this will be understood better with reference to the examples given below, the aim of which is to explain the advantages of the invention.

25

Example 1:

Example of mechanical chemical polishing of wafers with aqueous-suspension based abrasives with an colloidal silica acid pH and an anionic surfactant of secondary n-alkane sulphonate type.

30

There is a thermal growth of silicon oxide (called hereafter SiO₂)

6000 Å thick or a deposit of silicon nitride (called hereafter Si_3N_4) 1600 Å thick on each silicon wafer.

The wafers thus formed are then polished on a PRESI 2000 polisher with the following polishing conditions:

- 5 - bearing force: 0.5 daN/cm²
- turntable speed 30 r.p.m.
- head speed: 42 r.p.m.
- abrasive temperature: 10°C
- abrasive flow rate 100 cm³/min
- 10 - fabric: IC 1400 (Rodel)

With an aqueous acid suspension of colloidal silica, the characteristics of which are the following:

- pH of the aqueous suspension: 2.5
- average diameter of elementary colloidal silica particles: 50 nm
- 15 - concentration by weight of colloidal silica: 30%

and with a secondary alkane sulphonate-type surfactant having a chain length of 13 to 18 carbon atoms and average molecular weight of approximately 328, sold by Clariant under the name ®Emulsogen EP.

The following results were obtained:

20

Table no. 1

Test	EP Emulsogen %(vol)	SiO_2 Polishing speed (Å/min)	Si_3N_4 Polishing speed (Å/min)	$\text{SiO}_2/\text{Si}_3\text{N}_4$ Polishing selectivity
1	0.25	1151	9	128
2	0.50	1232	7	176
3	1.00	1273	9	141
4	0	1145	527	2

Relative to the specimen without surfactant (sample 4), the following was determined:

- a slightly increased polishing speed of the silicon oxide layer according to the quantity of anionic surfactant used,
- a very large decrease in the polishing speed of the silicon nitride layer according to the quantity of anionic surfactant used, even with small quantities of surfactant,

5 - as a result, a very greatly improved $\text{SiO}_2/\text{Si}_3\text{N}_4$ polishing selectivity increasing from 2 (without an anionic surfactant) to values comprised between 128 and 176 in the presence of 0.25 to 1% anionic surfactant,

- an excellent surface state of the polished silicon oxide layer,
- an improved silicon oxide polishing uniformity.

10

Example 2

With aqueous acid suspensions of colloidal silica and anionic surfactant similar to those in example 1, the polishing speed of a tetraethoxysilane (TEOS) layer deposited on a silicon wafer was studied relative to the polishing speed of a silicon nitride (Si_3N_4) layer deposited on a silicon wafer of the same type.

Under the same operating conditions as in example 1, but with an aqueous acid suspension of colloidal silica, the characteristics of which are the following:

20 - pH of the aqueous suspension: 2.5

- average diameter of elementary colloidal silica particles: 35 nm
- concentration by weight in colloidal silica: 30%

and with an anionic surfactant of secondary n-alkane sulphonate type, similar to that of example 1, the following results are obtained:

25

Table no. 2

Test	EP Emulsogen % (vol)	TEOS Polishing speed (Å/min)	Si ₃ N ₄ Polishing speed (Å/min)	TEOS/Si ₃ N ₄ Polishing selectivity
5	0.05	2007	98	20
6	0.10	1989	24	83
7	0.50	2193	7	313
8	1.00	2075	4	520
9	0	1905	438	4.4

Relative to a specimen without surfactant (sample 9), the following was determined:

5 - a slight increase in the polishing speed of the TEOS deposit when anionic surfactant is added to an aqueous acid colloidal silica polishing solution,

- a very large decrease in the polishing speed of silicon nitride the more the quantity of anionic surfactant used is increased,

- as a result, a very greatly improved TEOS/Si₃N₄ polishing selectivity increasing

10 from 4.4 (without anionic surfactant) to 520 (in the presence of 1% anionic surfactant of secondary n-alkane sulphonate type)

- an excellent surface state of the polished TEOS oxide layer

- an improved TEOS oxide polishing uniformity.

15 Example 3

Under the same operating conditions as in example 1, but with an aqueous acid solution of colloidal silica, the characteristics of which are the following:

- pH of the aqueous suspension: 2.5

20 - average diameter of the elementary colloidal silica particles: 35 nm

- concentration by weight of colloidal silica: 30%

and with a surfactant which is sodium dodecyl sulphate, the following results are obtained:

Table no. 3

Test	EP Emulsogen %(vol)	SiO ₂ Polishing speed (Å/min)	Si ₃ N ₄ Polishing speed (Å/min)	SiO ₂ /Si ₃ N ₄ Polishing selectivity
10	0.5	1224	18	68
11	0	1308	438	3

Relative to the specimen without surfactant (sample 11), the following

5 is determined:

- a slightly decreased polishing speed of the silicon oxide layer with 0.5% sodium dodecyl sulphate,
- a very large reduction of the polishing speed of the silicon nitride layer with 0.5% sodium dodecyl sulphate,

10 - as a result, a greatly improved polishing selectivity increasing from 3 (without sodium dodecyl sulphate) to 68 with 0.5% of sodium dodecyl sulphate.

Example 4

In the same operating conditions as in example 3, but polishing a

15 TEOS oxide layer instead of the thermal SiO₂ layer of example 3, the following results are obtained:

Table no. 4

Test	EP Emulsogen %(vol)	TEOS Polishing speed (Å/min)	Si ₃ N ₄ Polishing speed (Å/min)	TEOS/Si ₃ N ₄ Polishing selectivity
12	0.5	1687 (17%)	18	94
13	0	1905 (11%)	438	4.4

20 Relative to the specimen without surfactant (sample 13), the following is determined:

- a slight reduction in the polishing speed of the TEOS deposit when 0.5% sodium

dodecyl sulphate is added,

- a very large decrease in the polishing speed of silicon nitride,
- as a result, a very greatly improved TEOS/ Si_3N_4 polishing selectivity increasing from 4.4 (without surfactant) to 94 (in the presence of 0.5% sodium dodecyl sulphate).

Comparison example 1

Under the same operating conditions as example 1, but using an aqueous suspension of colloidal silica, the characteristics of which are the following:

- pH of the aqueous suspension: 7
- average diameter of the elementary colloidal silica particles 50 nm
- concentration by weight in colloidal silica: 30%

an a secondary n-alkane sulphonate-based surfactant similar to that of example 1, the following results were obtained:

Table no. 5

Test	EP Emulsogen %(vol)	SiO_2 Polishing speed ($\text{\AA}/\text{min}$)	Si_3N_4 Polishing speed ($\text{\AA}/\text{min}$)	$\text{SiO}_2/\text{Si}_3\text{N}_4$ Polishing selectivity
14	1.00	1690 (17%)	534	3.2
15	0	1662 (11%)	504	3.3

It was determined in this example that the use of a colloidal silica suspension with neutral pH in association with 1% EP Emulsogen does not modify the polishing speeds of the SiO_2 layer and the Si_3N_4 layer relative to the compositions without anionic surfactant, resulting in a non-improvement of the $\text{SiO}_2/\text{Si}_3\text{N}_4$ polishing selectivity which remains at the level of 3.2-3.3 which is poor within the scope of the present invention.

Comparison example 2

Under the same conditions as in example no. 1, but with a colloidal silica suspension, the characteristics of which are the following:

- pH of the aqueous suspension: 11
- 5 - average diameter of the elementary colloidal silica particles: 50 nm
- concentration by weight in colloidal silica: 30%

and secondary n-alkane sulphonate type surfactant similar to that of example 1, the following results are obtained:

10

Table no. 6

Test	EP Emulsogen %(vol)	SiO ₂ Polishing speed (Å/min)	Si ₃ N ₄ Polishing speed (Å/min)	SiO ₂ /Si ₃ N ₄ Polishing selectivity
16	0.50	1588	492	3.2
17	1.00	1556	501	3.1
18	0	1555	495	3.1

In this example, it is determined that the use of a colloidal silica suspension with basic pH in association with 0.5 to 1% EP Emulsogen does not modify the polishing speeds of the SiO₂ oxide layer and the Si₃N₄ layer, hence 15 there is no improvement in the SiO₂/Si₃N₄ polishing selectivity which remains at the level of 3.1 – 3.2, which is poor within the scope of the present invention.

Example 5

There is a deposit of a SiLK® type polymer with a low dielectric 20 constant 1000 Å thick on each silicon wafer, then the layer is polymerised by tempering at 450°C.

Under the same operating conditions as in example 1, but with an aqueous acid suspension of colloidal silica, the characteristics of which are the 25 following:

- pH of the aqueous suspension: 2.5
- average diameter of elementary colloidal silica particles: 50 nm
- concentration by weight in colloidal silica: 30%

and with an anionic surfactant of secondary n-alkane sulphonate type, similar to

5 that of example 1, the following results are obtained:

Table no. 7

Tests	EP Emulsogen %(vol)	SiLK Polishing speed (Å/min)	Polishing uniformity (%)
19	0.5	1695	14.5
20	0	14	Not significant

Relative to the specimen without surfactant (sample 20), the following

10 is determined:

- a very large increase in the polishing speed of the SiLK deposit when anionic surfactant is added to an aqueous acid colloidal silica polishing solution,
- a good SiLK deposit polishing uniformity when anionic surfactant is added to 15 an aqueous acid colloidal silica solution.

Comparison example 3

Under the same conditions as in example 5, but with a colloidal silica suspension, the characteristics of which are the following:

20

- pH of the aqueous suspension: 12
- average diameter of the elementary colloidal silica particles: 50 nm
- concentration by weight in colloidal silica: 30%

and secondary n-alkane sulphonate type surfactant similar to that of example 5,

25 the following results are obtained:

Table no. 8

Tests	EP Emulsogen %(vol)	SiLK Polishing speed (Å/min)	Polishing uniformity (%)
21	0.5	17	Not significant

In this example, it is determined that the use of a colloidal silica suspension with basic pH in association with 0.5 EP Emulsogen gives very low polishing speeds, which is poor within the scope of the present invention.

CLAIMS

1. An abrasive composition for the integrated circuit electronics industry, characterized in that it comprises an aqueous acid suspension of individualised colloidal silica particles not linked to each other by siloxane bonds and a surfactant.
2. An abrasive composition according to claim 1, characterized in that the pH of said composition is comprised between 1 and 5.
3. An abrasive composition according to one of claims 1 to 2, characterized in that the pH of said composition is comprised between 2 and 3.
4. An abrasive composition according to one of claims 1 to 3, characterized in that the average diameter of the abrasive particles of said composition is comprised between 12 nm and 100 nm.
5. An abrasive composition according to one of claims 1 to 4, characterized in that the average diameter of abrasive particles of said composition is comprised between 35 nm and 50 nm.
6. An abrasive composition according to one of claims 1 to 5, characterized in that the concentration by weight in abrasive particles of said composition is comprised between 5% and 50%.
7. An abrasive composition according to one of claims 1 to 6, characterized in that the concentration by weight in abrasive particles of said composition is comprised between 25 and 35%.
8. An abrasive composition according to one of claims 1 to 7, characterized in that the volumic concentration of the surfactant of said composition is comprised between 0.001% and 5%.
9. An abrasive composition according to one of claims 1 to 8, characterized in that the volumic concentration of the surfactant of said composition is comprised between 0.01% and 1%.
10. An abrasive composition according to one of claims 1 to 9, characterized in that the surfactant of said composition is of anionic or non-ionic

type.

11. An abrasive composition according to one of claims 1 to 10, characterized in that the surfactant of said composition is of anionic type.

12. An abrasive for mechanical chemical polishing in the 5 integrated circuits industry, characterized in that it comprises a fabric impregnated with an aqueous acid suspension of colloidal silica of pH comprised between 1 and 5 containing individualized particles not linked to each other by siloxane bonds of diameter comprised between 12 and 100 nm and a surfactant.

13. A process for mechanical chemical polishing in the integrated 10 circuits industry in which a layer of silicon oxide is rubbed using a support impregnated with an abrasive composition, characterized in that the abrasive composition comprises an aqueous acid suspension of individualized colloidal silica particles not linked to each other by siloxane bonds and a surfactant.

14. A process for mechanical chemical polishing in the integrated 15 circuits industry in which a layer of silicon nitride is rubbed using a support impregnated with an abrasive composition, characterized in that the abrasive composition comprises an aqueous acid suspension of individualized colloidal silica particles not linked to each other by siloxane bonds and a surfactant.

15. A process for mechanical chemical polishing in the integrated 20 circuits industry in which a selective mechanical chemical polishing of a silicon oxide layer and of another silicon nitride layer is carried out, characterized in that the abrasive composition comprises an aqueous acid suspension of individualized colloidal silica particles not linked to each other by siloxane bonds and a surfactant.

16. A process for mechanical chemical polishing in the integrated 25 circuits industry in which a layer of polymers with a low dielectric constant is rubbed using a support impregnated by an abrasive composition, characterized in that the abrasive composition comprises an aqueous acid suspension of individualized colloidal silica particles not linked to each other by siloxane bonds and a surfactant.

TITLE OF THE INVENTION

New abrasive composition for the integrated circuit electronics industry.

TEXT OF THE ABSTRACT

Abrasive composition for the integrated circuits electronics industry comprising an aqueous acid suspension of individualized colloidal silica particles not linked to each other by siloxane bonds and an abrasive surfactant, this abrasive being for mechanical chemical polishing in the integrated circuits industry, comprising a fabric impregnated by such a composition, and a process for mechanical chemical polishing.